AIMB-780

Intel[®] Core[™] i7/i5/i3/Pentium[®]/Xeon[®] ATX with DVI/VGA, 4 COM, Dual LAN, DDR3



Features

- Supports Intel[®] Core™ i7/i5/i3/Pentium[®]/Xeon[®] processor with Q57/3450 chipset
- Four DIMM sockets support up to 16 GB DDR3 800/1066/1333
- Supports dual display VGA and DVI and dual GbE LAN
- Supports SATA RAID 0, 1, 5, 10, AMT6.0, TPM1.2 (optional)
- Supports embedded software APIs and utilities

Software APIs:		H/W Monitor	Watchdog
Utilities:	Monitoring	Embedded Security ID	eSOS

CEFCC Note: eSOS need ODM BIOS by request

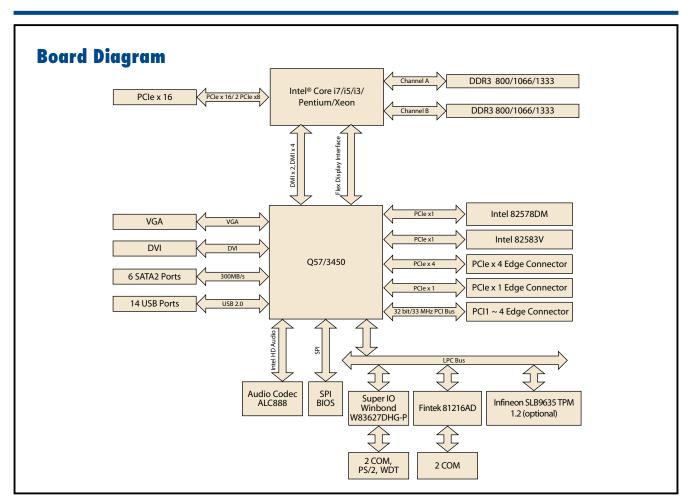
Specifications

shermann							
	CPU	Xeon 34xx	Core i7 8xx	Core i5 7xx	Core i5 6xx	Core i3 5xx	Pentium G69xx
	Core Number	4	4	4	2	2	2
	Speed	2.93 Ghz	2.93 Ghz	2.66 Ghz	3.3 GHz	3.06 GHz	2.8 GHz
	Integrated Graphics	-	-	-	Yes	Yes	Yes
Processor System	L3 Cache	8 MB	8 MB	8 MB	4 MB	4 MB	3 MB
TUCESSUI SYSTEIII	Support Model	WG2	QG2	QG2	WG2/QG2	WG2/QG2	WG2/QG2
	Socket	LGA1156					
	Chipset	G69XX CPUs)	ersion; 3450 for WG2 v	version (WG2 only su	upports Xeon 34xx, Co	ore i5 6xx, Core i3 5	x, and Pentium
	BIOS	AMI 64 Mbit SI					
	PCI	32-bit/33 MHz,	4 slots				
Summerican Olat	PCle x1	250 MB/s per o	lirection, 1 slot				
Expansion Slot	PCIe x 4	1.0 GB/s per di					
	PCle x16 (Gen2)	8 GB/s per dire	ction. 1 slot				
	Technology	Dual Channel D	DR3 800/1066/1333	(Only WG2 SKU sup	ports ECC memory)		
Memory	Max. Capacity	16 GB	,,	(-)			
lionicity	Socket	4 x 240-pin DI	MM				
	Controller		ics (Only the Core i5 6	ixx Core i3 and Pent	ium CPUs with dual c	ore are embedded w	ith integrated graphic:
Graphics	VRAM		shared memory with				in integrated graphiet
	Interface	10/100/1000 N			on nonory motaned		
Ethernet	Controller		82578DM, GbE LAN	2. Intel 82583\/			
	Connector	RJ-45 x 2	1 0201 0DIVI, ODE LAIN	2. 11101 02000 V			
	Max Data Transfer Rate	300 MB/s					
SATA	Channel	6					
	VGA	1					
	DVI	1					
	Ethernet	2					
Deer 1/0		-	an line t				
Rear I/O	USB	4 (USB 2.0 con					
	Audio	2 (Mic-in, Line	-out)				
	Serial	2 (RS-232)					
	PS/2		l and 1 x mouse)				
	USB	10 (USB 2.0 co					
	Serial	2 (1 x RS-232,	1 x RS-232/422/485 1	for support auto flow	control)		
	IDE	-					
Internal Connectors	SATA	6					
	FDD	1					
	Parallel	1					
	IrDA	-					
	DIO	-					
Watchdog Timer	Output	System reset					
watchuog miner	Interval	Programmable	1 ~ 255 sec/min				
	Power On		60 processor and disa	ble C state ; DDR3 2	GB x 4		
Power Requirement		5 V	3.3 V	12 V	5 Vsb	-12 V	
		4.23 A	0.96 A	3.93 A	1.18 A	0.01A	
		Operating			Non-Operating		
Environment			- 140° F), depends on	CPU sneed and			
Temperature		cooler solution		o. o opood and	-40 ~ 85° C (-40	J ~ 185° F)	
Physical Characteristics	Dimensions	304.8 x 244 mr					

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for

Ordering Information

Part Number	Chipset	Memory	USB	COM	GbE LAN	ТРМ
AIMB-780QG2-00A1E	Q57	Non-ECC	14	4	2	None
AIMB-780WG2-00A1E	3450	ECC/ Non-ECC	14	4	2	Optional

Riser Card

Part Number	Description
AIMB-RP10P-01A1E	1U riser card with 1 PCI expansion
AIMB-RP30P-03A1E	2U riser card with 3 PCI expansion
AIMB-RP3PF-21A1E	2U riser card with 1PCIe x16 & 2 PCI slot expansion
AIMB-RP3P8-12A1E	2U riser card with 2PCIe x8 & 1 PCI slot expansion (only WG2 Sku)

Bracket View



AIMB-780QG2-00A1E AIMB-780WG2-00A1E

Packing List

Part Number	Description	Quantity
1700003194	SATA HDD cable	x2
1703150102	SATA power cable	x2
1701092300	COM port cable kit	x1
1960019193T000	I/O port bracket	x1
2002078010	Startup manual	x1
2062078000	Driver CD	x1

Optional Accessories

Part Number	Description
1700002204	Dual port USB cable (27 cm) with bracket
1960047669N001	LGA1156 CPU cooler for 4U and wallmount chassis
1700008809	Printer port cable kit

Note: Purchasing AIMB-780's proprietary CPU cooler from Advantech is a must. Other brands' CPU coolers are NOT compatible with AIMB-780.

Value-Added Software Services

Software API: An interface that defines the ways by which an application program may request services from libraries and/or operating systems. Provides not only the underlying drivers required but also a rich set of user-friendly, intelligent and integrated interfaces, which speeds development, enhances security and offers add-on value for Advantech platforms. It plays the role of catalyst between developer and solution, and makes Advantech embedded platforms easier and simpler to adopt and operate with customer applications.

Software APIs

Control



General Purpose Input/Output is a flexible parallel interface that allows a variety of custom connections. It allows users to monitor the level of signal input or set the output status to switch on/off a device. Our API also provides Programmable GPIO, which allows developers to dynamically set the GPIO input or output status.



SMBus is the System Management Bus defined by Intel® Corporation in 1995. It is used in personal computers and servers for low-speed system management communications. The SMBus API allows a developer to interface a embedded system environment and transfer serial messages using the SMBus protocols, allowing multiple simultaneous device control.



I²C is a bi-directional two wire bus that was developed by Philips for use in their televisions in the 1980s.

The I²C API allows a developer to interface with an embedded system environment and transfer serial messages using the I²C protocols, allowing multiple simultaneous device control.

Display



The Brightness Control API allows a developer to interface with an embedded device to easily control brightness.



The Backlight API allows a developer to control the backlight (screen) on/off in an embedded device.

Software Utilities



The BIOS Flash utility allows customers to update the flash ROM BIOS version, or use it to back up current BIOS by copying it from the flash chip to a file on customers' disk. The BIOS Flash utility also provides a command line version and API for fast implementation into customized applications.



The embedded application is the most important property of a system integrator. It contains valuable intellectual property, design knowledge and innovation, but it is easily copied! The Embedded Security ID utility provides reliable security functions for customers to secure their application data within embedded BIOS.



Security ID

The Monitoring utility allows the customer to monitor system health, including voltage, CPU and system temperature and fan speed. These items are important to a device; if critical errors happen and are not solved immediately, permanent damage may be caused.

Monitor



A watchdog timer (WDT) is a device that performs a specific operation after a certain period of time if something goes wrong and the system does not recover on its own. A watchdog timer can be programmed to perform a warm boot (restarting the system) after a certain number of seconds.



The Hardware Monitor (HWM) API is a system health supervision API that inspects certain condition indexes, such as fan speed, temperature and voltage.



The Hardware Control API allows developers to set the PWM (Pulse Width Modulation) value to adjust fan speed or other devices; it can also be used to adjust the LCD brightness.

Power Saving



Make use of Intel SpeedStep technology to reduce power power consumption. The system will automatically adjust the CPU Speed depending on system loading.



Refers to a series of methods for reducing power consumption in computers by lowering the clock frequency. These APIs allow the user to lower the clock from 87.5% to 12.5%.



The eSOS is a small OS stored in BIOS ROM. It will boot up in case of a main OS crash. It will diagnose the hardware status, and then send an e-mail to a designated administrator. The eSOS also provides remote connection: Telnet server and FTP server, allowing the administrator to rescue the system.



Flash Lock is a mechanism that binds the board and CF card (SQFlash) together. The user can "Lock" SQFlash via the Flash Lock function and "Unlock" it via BIOS while booting. A locked SQFlash cannot be read by any card reader or boot from other platforms without a BIOS with the "Unlock" feature.

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