# PCI-1741U

## 16-bit, 200 kS/s Low cost Multifunction card w/A0



## **Features**

- 16-bit high resolution
- 200 kS/s sampling rate
- Auto calibration function
- 16 S.E. or 8 Diff. Al
- Unipolar/Bipolar input range
- 1 K samples FIFO for AI
- Universal PCI bus (support 3.3 V or 5 V PCI bus signal)
- BoardID™ switch

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## Introduction

PCI-1741U is a powerful high-resolution multifunction DAS card for the PCI bus. Its sampling rate is up to 200 kS/s and the 16-bit resolution makes it suitable for most data acquisition applications. PCI-1741U provides 16 single-ended or 8 differential analog input channels, one 16-bit D/A output channel, 16 digital input/output channels, and one 10 MHz 16-bit counter channel.

## **Auto-calibration Function**

PCI-1741U provides an auto-calibration function by using a calibration utility. The built-in calibration circuitry of the PCI-1741U corrects gain and offset errors in analog input and analog output channels thereby eliminating the need for external equipment and user adjustments.

#### BoardID™ Switch

PCI-1741U has a built-in BoardID™ DIP switch that helps define each card's unique identity when multiple identical PCI cards have been installed in the same computer. The BoardID switch is very useful when you build your system with multiple identical PCI cards. With the correct BoardID switch settings, you can easily identify and access each card during hardware configuration and software programming.

## **Plug & Play Function**

The PCI-1741U is a Plug & Play device, which fully complies with PCI Specification Rev 2.2. During card installation, there is no need to set jumpers or DIP switches. Instead, all bus-related configurations such as base I/O address and interrupt are automatically done by the Plug & Play function.

#### **On-board FIFO Memory**

The PCI-1741U provides 1K samples on-board FIFO (First In First Out) memory buffer for AD. This is an important feature for faster data transfer and more predictable performance under the Windows system.

## **On Board Programmable Timer/Counter**

The PCI-1741U provides a programmable timer counter for generating a pacer trigger for the A/D conversion. The timer/counter chip is 82C54, which includes three 16-bit counter 10 MHz clocks. One counter is used as an event counter for counting events coming from the input channel. The other two are cascaded together to make a 32-bit timer for pacer trigger time base.

## **Specifications**

## **Analog Input**

• **Channels** 16 single-ended or 8 differential or combination

Resolution 16-bit
FIFO Size 1 K samples
Max. Sampling Rate 200 kS/s

Innut yours and	Gain	0.5	1	2	4	8
Input range and Gain List	Unipolar	N/A	0~10	0~5	0~2.5	0~1.25
Gain List	Bipolar	±10	±5	±2.5	±1.25	±0.625
	Gain	0.5	1	2	4	8
Bandwidth for PGA	Bandwidth	4.0 MHz	4.0 MHz	2.0 MHz	1.5 MHz	0.65 MHz

• Common mode voltage ±11 V max. (operational)

Max. Input voltage ±20 V (protection)
 Input Protect 30Vp-p

■ **Input Impedance** 100 MΩ/10pF(0ff); 100 MΩ/100pF(0n)

	1	1				-		
		DNLE: ±1LSB						
		INLE: ±1LSB						
	DC	Zero (Offset) error: Adjustable to ±1 LSB					SB	
Accuracy	DO	Gain	0.5	1	2	4	8	
		Gain error (% FSR)	0.03	0.02	0.02	0.03	0.04	
	4.0	THD: -90 dB						
	AC	ENOB: 13.5 bits						
Clocking and Trigger			Software, on-board programmable pacer or external					
Inputs	A/D pacer clock	200 kHz (max.); 2.328mHz (min.)						

## **Analog Output**

Channels 1
Resolution 16-bit
Operation mode Single output

Throughput
 PC dependent, Software update (Direct AO)

Output Range (Internal & External	Using Internal Reference	0 ~ +5 V, 0 ~ +10 V, -5 ~ +5 V,-10 ~ +10 V		
Reference)	Using External Reference	$0 \sim +x \ V@ +x \ V \ (-10.x.10)$ -x \sim +x \ V@ +x \ V \ \ (-10.x.10)		
Accuracy		DNLE: ±1LSB (monotonic)		
		INLE: ±1LSB		
	DC Zero (Offset) error:Adjustable to ±1			
		Gain (Full-scale) error:Adjustable to ±1 LSB		
Dynamic	Settling Time	5µs (to 4 LSB of FSR)		
Performance Slew Rate		20 V/μs		

Drift 10 ppm/. Driving Capability ±20 mA - Output Impedance  $0.1\,\Omega$  max.

## **Digital Input /Output**

 Input Channels 16 - Output Channels 16 Number of Ports 2

Input Voltage	Low	0.8 V max.		
IIIput voitage	High	2.0 V min.		
Output Voltage	Low	0.5 V max. @ +24 mA (sink)		
	High 2.4 V min. @ -15 mA (so			
	High	2.0 V min.		

#### Counter/Timer

- Counter Chip 82C54 or equivalent

3 channels, 2 channels are permanently configured Channels

as programmable pacers; 1 channel is free for user

application

- Counter 0 16-bit counter

- Counter 1, 2 Cascade as a 32-bit clock divider for pacer clock for

A/D conversion

Resolution 16-bit

 Base Clock Channel 1: 10 MHz

Channel 2: Takes input from output of channel 1

Channel 0: Internal 100 kHz or external

 Compatibility TTL level

Clock Input	Low	0.8 V max.
	High	2.0 V min.
Gate Input	Low	0.8 V max.
	High	2.0 V min.
Counter Output	Low	0.5 V max. @ +24 mA (sink)
	High	2.4 V min @ -15 m∆ (source)

#### General

- I/O Connector Type 68-pin SCSI-II female Dimensions 175 x 100 mm (6.9" x 3.9")

Bower Consumption	Typical	+5 V @ 850 mA +12 V @ 600 mA	
Power Consumption	Max.	+5 V @ 1 A +12 V @ 700 m A	
Temperature	Operation	0 ~ 60 °C (32 ~ 158 °F) (refer to IEC 68-2-1, 2)	
	Storage	-20 ~ 70 °C (-4 ~ 185 °F)	

 Relative Humidity 5 ~ 95%RH non-condensing (refer to IEC 68-2-3)

 Certifications CE certified

## **Pin Assignments**

AI0 AI2 AI4 AI6 AI8 AI10 AI12 AI14 AIGND AO0_REF AO0_OUT AOGND DI0 DI2 DI4 DI6 DI8 DI10 DI12 DI14 DGND DO12 DI14 DGND DO0 DO2 DO4 DO6 DO8 DO10 DO12 DO14 DGND CNT0_CLK CNT0_OUT CNT0_GATE +12V	68 67 66 65 64 63 62 61 60 59 58 57 56 55 54 53 52 51 50 48 47 46 45 44 43 42 41 40 38 38 37 36 35	34 33 32 31 30 29 28 27 26 25 24 23 22 21 20 19 18 17 16 15 14 13 12 11 10 9 8 7 6 5 4 3 2 1	AI1 AI3 AI5 AI7 AI9 AI11 AI13 AI15 AIGND AO1_REF AO1_OUT AOGND DI1 DI3 DI5 DI7 DI9 DI11 DI13 DI15 DGND DO1 DO3 DO5 DO7 DO9 DO11 DO13 DO15 DGND PACER_OUT TRG_GATE EXT_TRG +5V

## **Ordering Information**

PCI-1741U 200 kS/s, 16-bit, 16-ch High-Resolution Multifunction Card, user's manual and driver CD-ROM. (cable not

included)

PCL-10168 68-pin SCSI-II cable with male connectors on both ends and special shielding for noise reduction, 1m. PCL-10168-2 68-pin SCSI-II cable with male connectors on both

ends and special shielding for noise reduction, 2m. - ADAM-3968 68-pin SCSI-II Wiring Terminal Board for DIN-rail

Mounting

■ PCLD-8710 Industrial Wiring Terminal Board with CJC circuit for

DIN-rail Mounting. (cable not included)

PCI-1741S PCI-1741U with PCLD-8710 and PCL-10168 cable