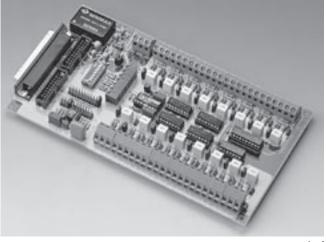
# PCLD-789D

## **Amplifier and Multiplexer Board**



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## Introduction

PCLD-789D is a front-end signal conditioning and channel multiplexing daughterboard for use with PC-LabCard™ product's analog input ports. It multiplexes 16 differential input channels into a single A/D converter input channel. You can cascade up to ten PCLD-789Ds, allowing a single data acquisition card to access 160 analog input channels.

**Features** 

measurement

2, 10, 50, 100, 200, 1,000

**Pin Assignments** 

•

shunt

- Multiplexes 16 differential inputs to one A/D input

Second connectors onboard allow daisy chaining

Expands a PC-LabCard<sup>™</sup> product's analog inputs to 128 channels

Screw-clamp terminal blocks permit easy and reliable connections

High-grade instrumentation amplifier provides switch selectable gains of 1,

Onboard cold-junction compensation circuits for direct thermocouple

Built-in signal conditioning functions include filter, attenuator and current

PCLD-789D has DB37 and 20-pin flat cable connectors and lets your PCL-818L or PCL-818HD access up to 128 channels without using an additional digital output cable to select channels

The PCLD-789D uses a high-grade instrumentation amplifier that provides switch-selectable gains of 1, 2, 10, 50, 100, 200 and 1,000. This amplifier lets you accurately measure low-level signals with your PC-LabCard™ product. The board also contains a cold-junction sensing circuit that allows direct temperature measurement from thermocouple transducers. A wide variety of thermocouples are supported with software compensation and linearization.

## **Specifications**

### I/O

•/	Cold-Junction Compensation	+24.4 mV/° C, 0 V at 0° C		ANA out 0 1 ANA out 1 3 ANA out 2 5 ANA out 3 7 ANA out 4 9
1	Input Channels Input Conditions	16 differential		ANA out 5 11 ANA out 6 13 ANA out 7 15 ANA out 8 17
-	Gains CM	RR Nonlinearity	Setting Time	ANA out 9 19
	1,000 125 100 115 10 105 1 85 c	dB         0.005% FSR           dB         0.005% FSR           dB         0.007% FSR	75 μsec. 15 μsec. 15 μsec. 15 μsec. 15 μsec.	D/1 0 1 3 D/1 2 5 7 9 11
	Input Range Output Range Overvoltage Protecti	±10 V maximum, depending ±10 V maximum ±30 V continuous	on the selected gain	13 15 D.GND 17 +5V 19
General				
:	Certifications Connectors	CE		erow and a second s
Controller: I/O:		1 x DB37 (male) connector 2 x 20-pin flat cable connectors for daisy chaining Screw terminals		44 44 44 5 5 5 5 5 5 5 5 5 5 5 5 5 5 5
<ul> <li>Dimensions (L x W)</li> </ul>		205 x 114 mm (8.1" x 4.5")	205 x 114 mm (8.1" x 4.5")	
<ul> <li>Mounting</li> <li>Device Concumption</li> </ul>			4 x screw holes for flat surface mounting	
•	Power Consumption	+5 V @ 30 mA max, +12 V @	Ψ δυ πια πιαχ	Land CON Safety CON Factor CON Factor CON Factor CON

#### A GND ANA out 2 ANA out 3 22 A.GND 23 10 12 14 16 18 A.GND ANA out 4 24 25 A.GND ANA out 5 A.GND ANA out 6 26 27 A-GND ANA out 7 A.GND A.GND 28 29 A GND A.GND 10 A.GND N/C 11 30 31 N/C N/C SO 12 13 D/I 1 D/I 3 2 4 6 8 10 12 14 16 18 20 +12V 32 S1 \$2 14 15 33 D.GND 34 35 D.GND N/C N/C 16 17 N/C 36 37 N/C D.GND +12V N/C 18 19 N/C +5V CLC CH SELECT CORE PROV Service Oncor 1.00 201 Concepts or Dava Ottan Is 4 15 Organe Sensal

#### **Block Diagram**

## **Ordering Information**

PCLD-789D

Amplifier and Multiplexer Board with DB37 connector and 20-pin flat-cable connectors. (Includes DB37 and 20-pin flat cable assemblies)

CN3

20 A.GND

21 A.GND

ANA out 0

ANA out 1

A.GNE